

On Constructing a Byzantine Linearizable SWMR Atomic Register from SWSR Atomic Registers

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Table of Contents

- 1 Research Objective
- 2 Introduction
- 3 Model and Preliminaries
- 4 Conclusion

Constructing a Byzantine Linearizable single-writer multi-reader register (SWMR) Atomic Register from single-writer single-reader (SWSR) Atomic Registers

Register Class

Register Class¹:

```
1.  public T read() {  
2.      return value;  
3.  }  
4.  public void write(Tv) {  
5.      value = v;  
6.  }
```

¹Maurice Herlihy and Nir Shavit. *The Art of Multiprocessor Programming, Revised Reprint*. 1st. San Francisco, CA, USA: Morgan Kaufmann Publishers Inc., 2012. ISBN: 9780123973375, 9780123977953.

Atomic Register

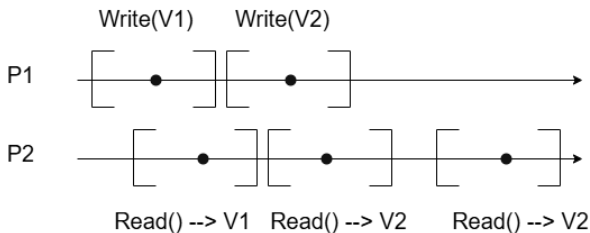


Figure: Atomic Register Example

History:² An execution of a system is modeled by a history, which is a finite sequence of operation (e.g., reads, writes) invocation and response events.

- 1 A history H defines a partial order $\prec_H$ on operations. $op_1 \prec_H op_2$ if the response event of op_1 precedes the invocation event of op_2 in H .
- 2 op_1 is concurrent with op_2 if neither precedes the other.

²Maurice Herlihy and Nir Shavit. *The Art of Multiprocessor Programming, Revised Reprint*. 1st. San Francisco, CA, USA: Morgan Kaufmann Publishers Inc., 2012. ISBN: 9780123973375, 9780123977953.

Linearizability of a Concurrent History

A *linearization* of a concurrent history H of object o is a sequential history H' such that:

- 1 After removing some pending operations from H and completing others by adding matching responses, it contains the same invocations and responses as H' ,
- 2 H' preserves the partial order $\prec_H$, and
- 3 H' satisfies o 's sequential specification.

Linearizable Histories Example

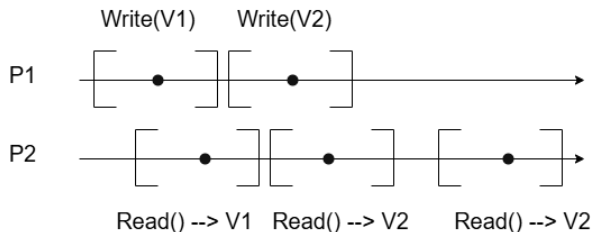


Figure: Regular Register Example

- ① R write(v1) P1
- ② R Read() P2
- ③ R OK() P1
- ④ R write(v2) P1
- ⑤ R OK(V1) P2
- ⑥ R Read() P2
- ⑦ R OK() P1
- ⑧ R OK(V2) P2
- ⑨ R Read() P2
- ⑩ R OK(V2) P2

Linearizable Histories Example

History H:

- ① R write(v1) P1
- ② R Read() P2
- ③ R OK() P1
- ④ R write(v2) P1
- ⑤ R OK(V1) P2
- ⑥ R Read() P2
- ⑦ R OK() P1
- ⑧ R OK(V2) P2
- ⑨ R Read() P2
- ⑩ R OK(V2) P2

History H':

- ① R write(v1) P1
- ② R OK() P1
- ③ R Read() P2
- ④ R OK(V1) P2
- ⑤ R write(v2) P1
- ⑥ R OK() P1
- ⑦ R Read() P2
- ⑧ R OK(V2) P2
- ⑨ R Read() P2
- ⑩ R OK(V2) P2

Fault Types:

- **Crash Fault:** A fault in which a process abruptly stops working and ceases all operations. The process does not recover or perform any additional actions.
- **Byzantine Fault:** A fault where a process may exhibit arbitrary behavior, including sending conflicting or incorrect information to other processes, often due to malicious or unpredictable issues.

Byzantine linearization of a concurrent history³: A history H is Byzantine linearizable if there exists a history H' such that $H'|_{correct} = H|_{correct}$ and H' is linearizable.

- $H|_{correct}$ denote the projection of history H to all correct processes.
- An object supports Byzantine linearizable executions if all of its executions are Byzantine linearizable.

³Shir Cohen and Idit Keidar. “Tame the Wild with Byzantine Linearizability: Reliable Broadcast, Snapshots, and Asset Transfer”. In: *35th International Symposium on Distributed Computing, DISC 2021*. Ed. by Seth Gilbert. Vol. 209. LIPIcs. 2021, 18:1–18:18. DOI: 10.4230/LIPIcs.DISC.2021.18. URL: <https://doi.org/10.4230/LIPIcs.DISC.2021.18>.

Register Linearizability

Register Linearizability⁴: In a system with Byzantine process failures, an implementation of a SWMR register is linearizable if and only if the following holds. If the writer is not malicious, then:

- (Reading a “current” value) If a read operation R by a process that is not malicious returns the value v then:
 - there is a write v operation that immediately precedes R or is concurrent with R , or
 - $v = v_0$ (the initial value) and no write operation precedes R .
- (No “new-old” inversion) If two read operations R and R' by processes that are not malicious return values v_k and $v_{k'}$, respectively, and R precedes R' , then $k \leq k'$.

⁴Xing Hu and Sam Toueg. “On Implementing SWMR Registers from SWSR Registers in Systems with Byzantine Failures”. In: *36th International Symposium on Distributed Computing, DISC 2022, , Augusta, Georgia, USA*. vol. 246. LIPIcs. 2022, 36:1–36:19. DOI: 10.4230/LIPICS.DISC.2022.36.

Example of Byzantine Linearizable Register

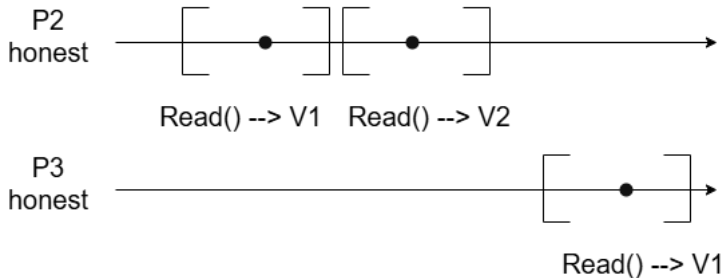


Figure: Byzantine Linearizable Register History

Example of Byzantine linearizable

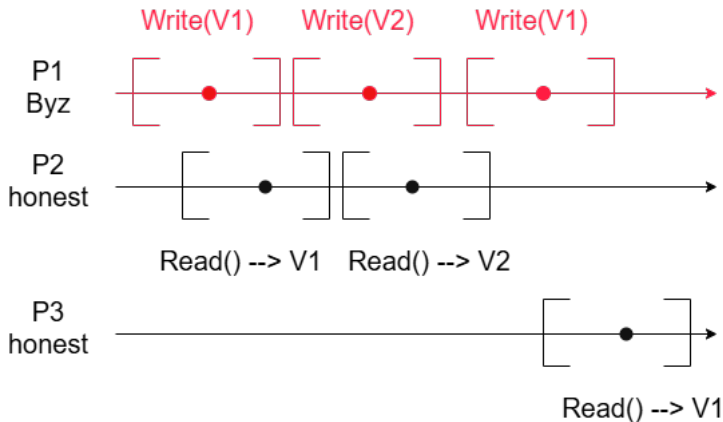


Figure: Byzantine Linearizable Register History

Stronger Byzantine Linearizable SWMR Object

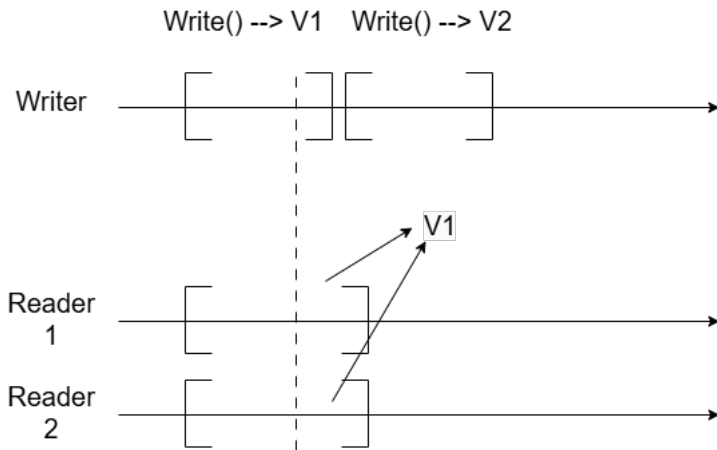


Figure: Byzantine SWMR Object

Stronger Byzantine Linearizable SWMR Object

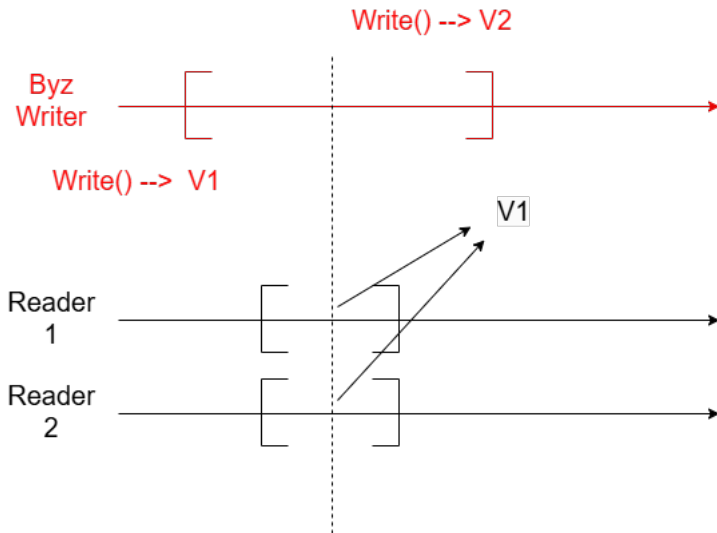


Figure: Byzantine SWMR Object

pseudo-correct operation definition

A pseudo-correct operation is an operation executed by a process where it results in a value being returned to honest processes, is indistinguishable from the steps of a correct operation.

Example Byzantine linearizable

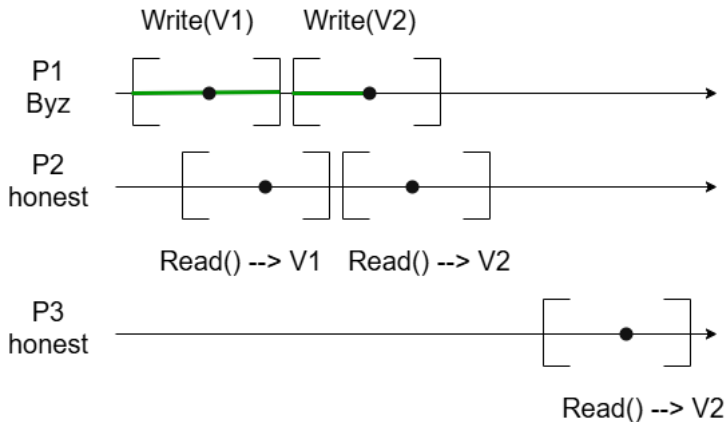


Figure: Byzantine Linearizable Register

Register Linearizability

Register Linearizability In a system with Byzantine process failures, an implementation of a SWMR register is linearizable if and only if the following two properties are satisfied in a Byzantine linearization of a concurrent history.

- ① **Reading a current value:** When a read operation R by a non-Byzantine process returns the value v :
 - ① if $v = v_0$ then no correct or pseudo-correct write operation precedes R
 - ② else if $v \neq v_0$ then v was written by the correct or pseudo-correct write operation that immediately precedes R .
- ② **No “new-old” inversions:** If read operations R and R' by non-Byzantine processes return values v^i and v^j , respectively, and R precedes R' , then $i \leq j$.

Conclusion

- We have studied Byzantine tolerant construction of a SWMR atomic register from SWSR atomic registers
- We have introduced the notion of a pseudo-correct write operation by a Byzantine writer, which has the effect of a correct write operation
- A definition of stronger Byzantine linearizable register object by non-trivially taking into account Byzantine behavior of the writer and readers

Thank you