### Two Way Generalization of a Binary Burst Erasure Correcting Code

Myna Vajha

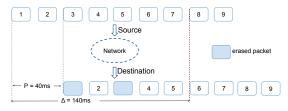
EE Deparment, IIT Hyderabad

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Joint Work With Vinayak Ramkumar (Postdoc, Tel Aviv), Nikhil Krishnan (Asst. Professor, IIT Palakkad), P. Vijay Kumar (Hon. Professor, IISc)

### Motivation: Latency Sensitive Applications

• Several latency sensitive applications (real-time audio/video, AR/VR etc) that have end-to-end (E2E) latency requirements under packet erasures



 $\Delta$ : E2E delay,  $\tau$  = 4: Delay in packet count

- E2E delay modeled through count of packets accessed in future.
- Goal: Design packet level FECs that can recover erasures within delay  $\tau$ .

### Delay Profile Of a Block Code

- An (n, k) linear block code can guarantee a worst case delay of (n 1).
  - ► systematic case, the message symbols have delay profile (n-1, n-2, n-3, · · · , n-k).
- For a worst-case delay guarantee of τ, in presence of burst erasures of size b, any burst correcting code with parameters n = τ + 1, k = n - b will work.

### Can we do better (rate)?

- Yes. Can construct  $(n = \tau + b, k = n b)$  linear block code with delay profile  $(\tau, \dots, \tau, \tau, \tau, \tau 1, \dots, b)$ . (details to follow)
  - ▶ *i*-th message symbol with delay requirement < n − *i* will be referred to as *urgent symbol*. Otherwise it is non-urgent symbol.

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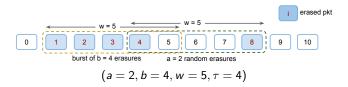
- Yes. Can construct  $(n = \tau + b, k = n b)$  linear block code with delay profile  $\underbrace{(\tau, \cdots, \tau, \tau, \tau, \tau 1, \cdots, b)}_{(b-1)}$ . (details to follow)
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E. Martinian and M. Trott, "Delay-Optimal Burst Erasure Code Construction," ISIT 2007

# Erasure Model: Delay Constrained Sliding Window (DC-SW) Channel

(i) Admissible erasure patterns (AEP): within a sliding window of size W: either  $\leq a$  random erasures, or a burst of  $\leq b$  erasures

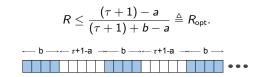
(ii) Decoding-Delay Parameter: au



Badr et al., "Layered Constructions for Low-Delay Streaming Codes," Trans. IT, 2017.

### Streaming Codes and Optimal Rate

- Streaming code is a packet-level FEC that can correct from all AEP of DCSW channel within a decoding delay constraint  $\tau$ .
- It turns out that WOLOG, we can assume  $w = \tau + 1$ .
- The rate *R* of an  $\{a, b, \tau\}$  streaming code has the upper bound:

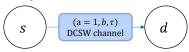


• Rate is 0 if  $\tau < b$ . Non-trivial only when  $a \le b \le \tau$ .

- Badr et al., "Layered Constructions for Low-Delay Streaming Codes," Trans. IT, 2017.
- M. Vajha, V. Ramkumar, M. Jhamtani and P. V. Kumar, "On the Performance Analysis of Streaming Codes over the Gilbert-Elliott Channel," ITW 2021

### Settings Discussed in This Talk

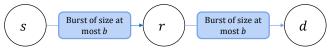
S1: Single link, burst erasures only



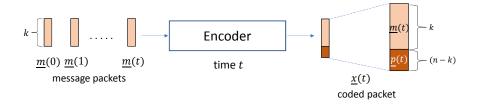
S2: Single link, burst or random erasures



S3: 3-node relay, burst erasures only, delay constraint au



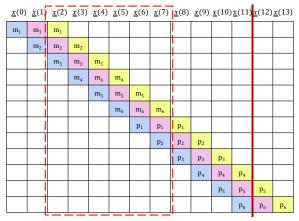
### Redundancy through Packet Expansion Framework



• Can use scalar block codes to come up with streaming codes.

# Diagonal Embedding (DE)

- Codewords of [n, k] scalar block code are diagonally placed in the packet stream.
- This approach needs  $n k \ge b$

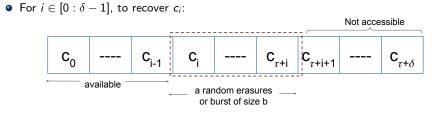


DE of [12, 6] scalar code where  $a = 4, b = 6, \tau = 9$ 

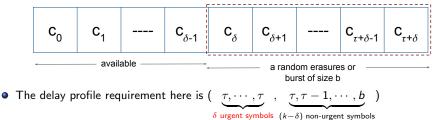
### Scalar Code Properties

• For a given  $\{a, b, \tau\}$  let

$$n = \tau + 1 + \delta, k = n - b$$
 where  $\delta = b - a$ 



Let E ⊂ [δ : τ + δ] be either the set of a random erasures or a set of consecutive b erasures. To recover {c<sub>j</sub> | j ∈ E}:



•  $\delta = b - 1$  when a = 1 for the burst only case.

### S1: Single Link, Burst Erasures

• 
$$R_{\text{opt}} = \frac{\tau}{\tau+b}$$
 (by setting  $a = 1$  in  $R_{\text{opt}}$ )

 Use (τ, τ – b) wrap-around burst correcting code (WA-BCC) to construct (a = 1, b, τ) streaming code with (n = τ + b, k = n – b).

$$H_{WB} = \left[ \begin{array}{cc} P & I_b \\ (b \times \tau - b) & \end{array} \right]$$

•  $(n = \tau + b, k = n - b)$  code with pc matrix H is an  $(a = 1, b, \tau)$  streaming code

$$H_{WBS} = \begin{bmatrix} I_b & P & I_b \end{bmatrix}$$

- Clearly burst erasure correcting code
- What about worst case delay ?
- $i \in [0: b-1]$ ,  $m_i$  requires delay au, (until *i*-th parity available)
- ▶ *m*<sup>0</sup> recovery: use 0-th pc
- $m_i$  recovery  $i \in [1 : b 1]$ : need a pc equation with 0's in locations  $[i+1:i+b-1], [\tau+i+1:n-1]$ .

### S1: Single Link, Burst Erasures

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#### E. Martinian and M. Trott, "Delay-Optimal Burst Erasure Code Construction," ISIT 2007

### S1: Single Link, Burst Erasures

mi recovery

$$H_{WB} = \begin{bmatrix} P & I_b \end{bmatrix}, \quad H_{WBS} = \begin{bmatrix} I_b & P & I_b \end{bmatrix}$$
$$H_{WBS}([0:i],:) = \begin{bmatrix} I_{i+1} & 0 \\ (i+1) \times (b-i-1) & P([0:i],:) & I_{i+1} & 0 \\ (i+1) \times (b-i-1) & P([0:i],:) & I_{i+1} & 0 \end{bmatrix}$$

• "top-left" 
$$(i \times i)$$
 sub-matrix of  $P$  (say  $P_i$ ) is invertible for any  
 $i \in [\min\{b, n-b\} - 1]$   
•  $\exists v \in \mathbb{F}_2^i$  such that  $[v^T \ 1]P([0:i], [0:i-1]) = 0.$   
 $\begin{bmatrix} v^T \ 1 \end{bmatrix} H_{WBS}([0:i], :) = [v^T \ 1 \ \underbrace{0}_{b-1} X \ v^T \ 1 \ \underbrace{0}_{b-i-1}]$ 

- if n − b < b, any n − b "consecutive-rows" of P are l.i. Can show m<sub>i</sub> recoverable in delay for i ∈ [n − b : b − 1]
- Hollmann and Toulhuizen (HT) code is a binary WA-BCC.

E. Martinian and M. Trott, "Delay-Optimal Burst Erasure Code Construction," ISIT 2007

### WA-BCC: Hollmann and Toulhuizen (HT) Code

• Recursive construction of  $(u \times v)$  matrix  $P_{u,v}$  that appends identity matrices column-wise or row-wise.

$$P_{u,v} = \begin{cases} [I_u \ P_{u,v-u}] & v > u \\ \begin{bmatrix} I_v \\ P_{u-v,v} \end{bmatrix} & \text{otherwise} \end{cases}$$

- Let  $H_{WB} = [P \ I_b]$  be the pc matrix of (n, n b) WA-BCC. P is an  $(b \times n b)$  matrix.
  - clear that  $H_{WB}^{col} = [I_b P I_b]$  is pc matrix of (n + b, n) WA-BCC.
  - Can show that row extension also retains the WA-BCC (using the fact that dual of WA-BCC is also WA-BCC)

$$H_{WB}^{\text{row}} = \left[ I_n \mid \frac{I_{n-b}}{P} \right]$$

H. D. L. Hollmann and L. M. G. M. Tolhuizen, "Optimal Codes for Correcting a Single (Wrap-Around) Burst of Erasures," in TIT

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- Let H<sub>WB</sub> = [P I<sub>b</sub>] be the pc matrix of (n, n − b) WA-BCC. P is an (b × n − b) matrix.
  - clear that  $H_{WB}^{col} = [I_b P I_b]$  is pc matrix of (n + b, n) WA-BCC.
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$$H_{WB}^{\rm row} = \left[ \begin{array}{c} I_n & \boxed{I_{n-b}} \\ P \end{array} \right]$$

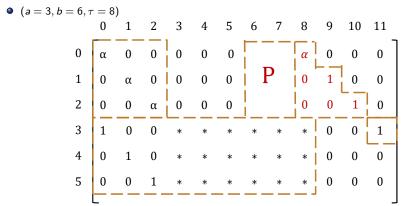
H. D. L. Hollmann and L. M. G. M. Tolhuizen, "Optimal Codes for Correcting a Single (Wrap-Around) Burst of Erasures," in TIT

# S2: Single Link, Random and Burst Erasures JigSaw code

•  $(a = 3, b = 6, \tau = 8)$  streaming code defined by  $(6 \times 12)$  parity check matrix 0 0 0 α 0 0 α  $v_{0,6} v_{0,7}$  $v_{1.9}$  $L^0$ α  $v_{08}$  $v_{1,9}$ \* \* \* \* 1 0 \* \* \* \* \* \* 0 1 \* \* \* \* \* 

JigSaw is rate-optimal streaming code for any (a, b, τ) with field size of q<sup>2</sup> where q ≥ τ.
α ∈ F<sub>q<sup>2</sup></sub> \ F<sub>q</sub>

M. Nikhil Krishnan, Deeptanshu Shukla and P. Vijay Kumar, "Rate-Optimal Streaming Codes for Channels With Burst and Random Erasures", IEEE Trans. Info. Theory, 2020



• Last *a* = 3 rows of the pc matrix unchanged. Random erasure recovery follows from the structure of JigSaw.

• Support of the first  $\delta = b - a = 3$  rows changed construction. Matrix P has 0, 1 elements.

M. Vajha, V. Ramkumar, M. N. Krishnan and P. Vijay Kumar, "Explicit Rate-Optimal Streaming Codes With Smaller Field Size," in IEEE ISIT 2021, Trans. Info. Theory, 2024

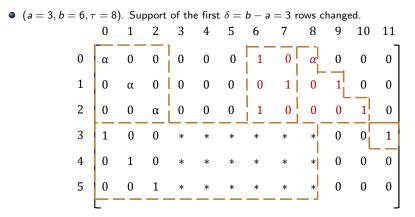
• The definition of  $(u \times v)$  matrix  $P_{u,v}^a$  is recursive.

$$\mathbf{P}_{u,v}^{a} = \begin{cases} \begin{bmatrix} I_{u} & \underbrace{\mathbf{0}}_{(u \times a)} & \mathbf{P}_{u,v-u-a}^{a} \end{bmatrix} & u + a < v \\ \begin{bmatrix} I_{u} & \underbrace{\mathbf{0}}_{(u \times (v-u))} \end{bmatrix} & u \le v \le u + a \\ \begin{bmatrix} I_{v} & \\ \mathbf{P}_{u-v,v}^{a} \end{bmatrix} & v < u \end{cases}$$

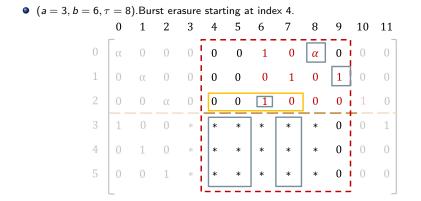
- This structure of the P matrix results in two properties on  $\hat{P} = [\underbrace{0}_{P} P]$ 
  - consecutive-columns: any b consecutive columns of P̂ have a-zero columns and δ linearly independent columns.
  - *bottom-right:* bottom right sub-matrix of  $\hat{P}$  of size  $\theta \times (\theta + a)$ , there are *a*-zero columns and  $\theta$  linearly independent columns.
  - top-left, consecutive-rows properties hold for P.

$$P_{3,2}^{3} = \begin{bmatrix} h_{2} \\ P_{1,2}^{3} \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \\ 1 & 0 \end{bmatrix}$$

 $\delta \times a$ 

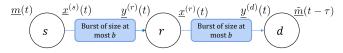


 top-left, consecutive rows properties can be used to show urgent symbol recovery from burst.



 bottom-right, consecutive columns properties used to show burst recovery of non-urgent symbols

Rate Bound



• An example, permissible erasure pattern in the R-D link

$x^{r}(t)$		$x^r(t+\tau-b)$	$x^r(t+\tau-b+1)$		$x^r(t+\tau)$
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- m(t) should be recovered at relay by  $t + \tau b$
- Rate in the S-R link upper bounded by rate of  $(a = 1, b, \tau b)$  DCSW channel.
- (b, τ) streaming code for S3 satisfies

$$\mathsf{R} \leq egin{cases} rac{ au-b}{ au} & au \geq 2b \ 0 & ext{otherwise} \end{cases}$$

• Generalized HT based streaming code achieves rate arbitrarily close to the upper bound.

V. Ramkumar, M. Vajha, M. N. Krishnan, "Streaming Codes for Three-Node Relay Networks With Burst Erasures", to appear in

### S3: 3 Node Relay, Burst Erasures S-R link

- $(n = \tau, k = \tau b)$  HT code is used at the source.
- Example:  $\tau = 10, b = 4$ ,  $(4 \times 10)$  pc matrix

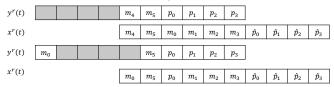
$$H_{HT} = \begin{bmatrix} I_4 \mid P_{4,2} \mid I_4 \end{bmatrix} = \begin{bmatrix} P_{4,6} \mid I_4 \end{bmatrix} = \begin{bmatrix} I_4 \mid \frac{I_2}{I_2} \mid I_4 \end{bmatrix}$$

$$p_0 = m_0 + m_4, p_1 = m_1 + m_5, p_2 = m_2 + m_4, p_3 = m_3 + m_5$$

Transmit at Source

t	0	1	2	3	4	5	6	7	8	9	10	11	12	13
$x^{s}(t)$	$m_0$	$m_1$	$m_2$	$m_3$	$m_4$	$m_5$	$p_0$	$p_1$	$p_2$	$p_3$				

• Receive at Relay: Burst starting at time 0/1 in S-R link



#### R-D Link



Burst at 1 in S-R link

• Information needs to be preserved.

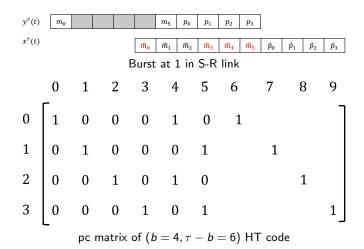
• Can resolve  $m_4$  from  $p_0 = m_0 + m_4$ , and  $m_0$ 

• 
$$\alpha = \min\{\beta, b-1\}$$
 where  $\beta$  is burst start and set  $(\hat{m}_0, \cdots, \hat{m}_{k-1})$  as

$$(\underbrace{m_0, \cdots, m_{\alpha-1}}_{\alpha \text{ urgent symbols}}, \underbrace{x^s(\alpha), \cdots, x^s(k-b+\alpha-1)}_{(k-\alpha) \text{ non-urgent symbols}}, \underbrace{m_\alpha, \cdots, m_{b-1}}_{(b-\alpha) \text{ urgent symbols}})$$

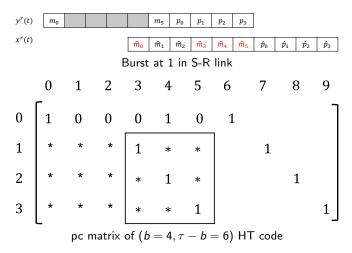
- HT code guarantees the information preservation property
- $\blacktriangleright$  GHT code parameterized by  $\alpha.$  Slight overhead to communicate  $\alpha$  to destination
- Urgent messages:
  - For  $i \in [0 : \alpha 1]$   $\hat{m}_i$  requires delay  $\leq \tau b$  (until  $\hat{p}_i$  available)
  - For  $i \in [0: b \alpha 1]$   $\hat{m}_{k-1-i}$  requires delay  $\leq b$  (until  $\hat{p}_{b-1-i}$  is available)

GHT construction



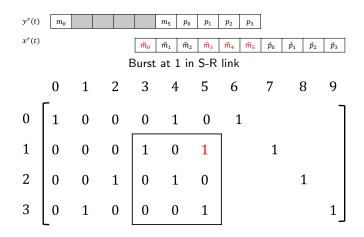
• Retain row 0, ensures in time recovery of  $\hat{m}_0$ 

**GHT** Construction



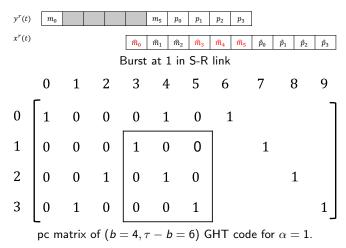
• Structured Bottom-Right Property: Permute rows of parity check matrix to get 1 in the diagonal of bottom-right  $(b - \alpha) \times (b - \alpha)$  sub-matrix

### S3: 3 Node Relay, Burst Erasures GHT Construction



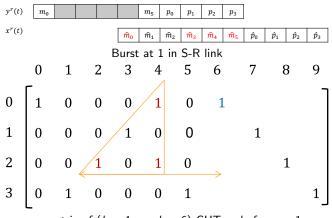
• Set the upper triangular elements to 0

**GHT** Construction



- $\hat{m}_3, \hat{m}_5$  can be recovered in delay
- $\hat{m}_1$  can be recovered from any burst

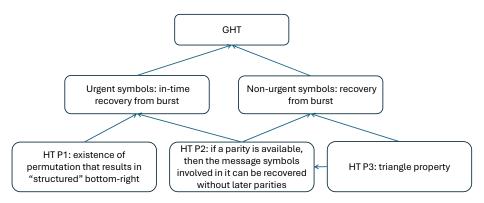
**GHT** Construction



pc matrix of ( $b = 4, \tau - b = 6$ ) GHT code for  $\alpha = 1$ .

- $\hat{m}_4$  recovery: if  $\hat{m}_2$  is available can recover it using  $\hat{p}_2$  otherwise from  $\hat{p}_0$ .
- $\hat{p}_0$  and  $\hat{m}_2$  are spaced by *b* by triangle property
- Can recover *m<sub>i</sub>* within delay.

### Key Ideas in General Proof



### Future Directions

• Construction of codes for any ordering of the urgent symbols  $(m_0, \cdots, m_{b-1})$  that are "allowed".

 $(m_0, *, *, m_1, m_2, m_3, p_0, p_1, p_2, p_3)$   $\checkmark$  $(m_1, *, *, m_0, m_2, m_3, p_0, p_1, p_2, p_3)$   $\checkmark$ 

- leads to streaming codes for *m*-node relay settings
- 3-node relay settings with different size bursts  $b_1, b_2$ .
- Characterize the delay-profiles for which constructions are possible

### Thank You!