K R SAI PRANAV

SKILLS

TECHNICAL

- Embedded C
- Python
- C ++ / C
- Basic Understanding of Hadoop and Distributed Computing
- Basic Understanding of Linux

NON - TECHNICAL

- Critical Thinking
- Analytical Reasoning
- Quick Learner and Adaptive to technology
- Strong Observation skills

LANGUAGES

- Telgu (Mother Tongue)
- Kannada
- English

Get in touch!

Mobile:
+91-9742494274

Email:
ai20mtech14013@iith.ac.in

Address:
#301, Arya Comforts, Padmanabhanagar, Bangalore - 560070

Professional Goals

It is my endeavor to be a part of an organization that demands innovation, disciple, and work in an environment where I can contribute to organizational growth along with career advancement besides appropriate recognition.

Work Experience

Firmware Developer

Robert Bosch Engineering and Services | July 2017 - March 2019

- Lead developer and POC (Point of Contact) for a project on Update dongle USB drive, used to update variable frequency drives (VFD).
- Received appreciation for taking responsibility for two major modules in the "Update Dongle" project.
- Development of firmware for VFD (Variable Frequency drive) which are widely used in industries such as wood cutting machines, conveyor belts.
- Firmware developer in the new architecture design and development of variable frequency drives.

Academic History

IIT Hyderabad

MTech (August 2020 - Current) | Department of AI (Artificial Intelligence)

- CGPA - 8.33

Bangalore Institute of Technology

Electronics and Communication Engineering | August 2013 - August 2017

- Aggregate - 78.83%
- Member of ECSA (Electronics and Communication Student Association).
- Won Best Marketing Presentation team award in E-Bike Championship.
- Participated in Bluetooth Controlled Robot Workshop.

Thesis Project : IP-RATProject - AI CAD

January 2021 - Currently

- The project aims at developing a neural network that will predict the outputs of backend EDA flow at a very early stage using GCNs.
- Phase 1 which is currently ongoing has major expected predictions for the parameters namely power, performance (timing values), and chip density.

Project : AFCS Integration Test Bench

- Developed an integrated test kit to test the LRUs (Line Replacement Units) in a real-time environment to find out the faulty unit and simulate the same on a helicopter in association with Hindustan Aeronautics Limited.