PRASANNATA BHANGE

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Temporary Address:
IIT Hyderabad Kandi Campus,
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Objective:

To work with an organization where I can give best of my knowledge and skills to add to that organization and gain further skills and knowledge.

Education:

Course of Study	SCHOOL/COLLEGE	Board/University	Year of passing	Result
M.Tech	R.C.O.E.M. Nagpur	RTM Nagpur University	2019	8.07 CGPA
B.E.	G.H. Raisoni Institute of Engineering and Technology for Women Nagpur	RTM Nagpur University	2016	7.60CGPA
HSC(XII)	St. Ursula Jr. college, Nagpur.	Maharashtra State Board	2012	72.50%
SSC (X)	Providence Girls' High school, Nagpur.	Maharashtra State Board	2010	88.73%

Experience:

14 August 2018 | Intern

| Indian Institute of Technology, Hyderabad

Presently working as Project Staff in IIT Hyderabad.

Academic Projects:

POST GRADUATION LEVEL:

Title : Android Implementation on Zynq UltraScale+MPSoC (ZCU102) platform Targeting Drone Navigation Applications.

- This project has been carried out in **Indian Institute of Technology Hyderabad** (IITH).

Duration: 12 months

Description:

- Android OS has been the platform to control the flight controller of drone.
- Android OS resides on the Zynq UltraScale+MPSoC (ZCU102) FPGA board.
- Android application has been developed which runs on this Android OS of ZCU102 FPGA kit.
- Android Application has been developed using the Mobile SDK provided by the Drone company.
- Android application is about automating the drone flight or path.

Skills Developed:

- JAVA programming language
- Basics of Drone working.
- Working on AOSP (Android Open Source Project)

UNDER GRADUATION LEVEL:

Title : Design and Xilinx Implementation of Image Compression Algorithm using DCT.

Duration : 10 months

Description :
- This project dealt with compressing an image to access big size images easily.
- By mapping pixels of MATLAB into Xilinx they were analyzed and compressed.

Skills Developed:

- MATLAB coding

Title : USB Mobile Charger (Mini Project)

Duration : 2 months

Description :
- This project dealt with designing portable USB mobile charger for efficient mobile recharging purpose.
- Simple circuit consisting of resistor, capacitor and transistor was designed on a PCB by using.

Skills Developed:

- PCB designing using Eagle.

Skill Set:

Hardware Description Languages : VHDL, Verilog, System Verilog.

Software Skills	:	C/C++, Python, JAVA(Beginner)
Software Tools	:	MATLAB, Keil µVision, ISE Design Suite, Vivado, Questa Sim, Spyder, Mentor Graphics (Pyxis), Electric tool, Spice,
Hardware Expertise	:	FRDM-KL25Z ,Nexys 4 Artix-7 Board .

Extra-Curricular:

- Executive member in technical event (organized by college).
- Won two Titles in Fashion Show in collegiate and intercollegiate level.
- Actively participated in technical events of other colleges and also attended workshops (Robotics, Techfest IIT Bombay).
- Student coordinator in Cisco workshop.

Personal Snippets:

Date of Birth	**:**	May 24,1994.
Gender	**:**	Female
Language known	**:**	Marathi, Hindi and English.

Prasannata Bhange