Student Profile

Indian Institute of Technology Hyderabad

Basic Summary

Student Name : D Jaswanth

Gender : Male

Date Of Birth : 08/07/1994

Summary : Strong interest in Digital System Design
Desire to associate with the VLSI industry and a challenging career in a progressive organisation where one can acquire current and relevant knowledge in leading edge technologies which enhances individual and organisational productivity.For M-tech thesis as a part of curriculum working on a project "Reconfigurable Framework for Convolutional Neural Networks on Resource Constraint Platform".

Extracurricular Activities : Selected for Top30 “DESIGNS CONTEST” conducted by “ELECTRONICS FOR YOU” and received a Scholarship of Rs 5000.
Interested in Social Works.
Interested in Cricket and Football.

Current Degrees

Degree	Batch
M.Tech Micro-Electronics & VLSI	2018AUG

Prior Qualifications

Degree	Degree Category	Institute	Year	Score Type	Score
10th	Full Time	New Horizon HS Kurmannapalem, Secondary School Certificate, Visakhapatnam	2009	Percentage	89.83
10+2	Full Time	B H P V Senior Secondary School,Central Board of Secondary Education, Visakhapatnam	2011	Percentage	72.40
B.Tech Electronics & Communication Eng	Full Time	Vellore Institute of Technology (VIT)	2015	CGPI(Scale of 10)	8.30

Skills

Skill	Version	Skill Level	Last Year Used	Years Used	Months Used
Synthesis Tools	Synopsis DC and VCS	Beginner	2019	0	2
Simulation	Comsol	Beginner	2019	0	2
Simulation	T Cad	Beginner	2019	0	2
C	DevC++	Intermediate	2019	1	0
Verilog	Modelsim10.4a	Intermediate	2019	1	0
MATLAB	R2018b	Intermediate	2019	1	0
Analog Circuit Design	Modelsim 10.4a	Intermediate	2019	1	0

Areas Of Interest

To design an Architecture and develop Hardware for Algorithms using HDL in Real Time Applications.

Project

Title : Reconfigurable Framework for Convolutional Neural Networks on Resource Constraint Platform

Client :

From : Jun-2019

To : May-2020

Role : Programmer & Designer

Role Description : FPGA Implementation of CNN Algorithm for different models

Team Size : 1

Project Location : Hyderabad

Skills Used : Matlab,Verilog,FPGA

Project Details : Convolutional Neural Network (CNN) are Deep learning Algorithms which are very accurate for detecting important features in Image Processing & Bio-Medical Applications. For Real time Applications on Edge devices like Smartphones it is computationally efficient which uses special convolution and pooling operations and performs parameter sharing.Idea of reconfigurability is using same Architecture for different models

Work Experience

Current Job	: No	**Job Type**	: Permanent : Full Time
Company Name	: TVR Polytechnic	**Designation**	: Lecturer
From	: Aug-2015	**To**	: Mar-2018
Job Profile	: Teaching		

Work Domain

Domain : Education

Languages

Language	Read	Write	Speak	Proficiency
Hindi	Y	Y	N	Beginner
Telugu	Y	Y	Y	Proficient
English	Y	Y	Y	Proficient
German	Y	N	Y	Beginner