Anmol Singh Narwariya

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Area of Interest

- Field Programmable Gated Array, Hardware/Software Co-design, Hardware Interfacing, Real-Time Operating System & Machine Learning.

Employment History

June 2019 -- December 2019	**Project Assistant** at CSIR-Central Electronics Engineering Research Institute, Pilani. Here, I did video stream using Gstreamer pipeline with the h264 encoding. and also did this video data stored in the NAS device. This all work was did on Nvidia Jetson TX2 hardware.
January 2020 -- July 2020	**Project Scientist** at Indian Institute of Technology, Delhi. Here, I am interface multiple sensor (Pms7003, BME, GPS, MPU-6050) with STM32 micro-controller and sending this sensor data to the cloud.

Education

2017 -- 2019

M.Tech. in Embedded Syestem: Malaviya National Institute of Technology, Jaipur
CGPA: **8.15**
Thesis title: *Real-Time Operating System Mapping for Task Performance Evaluation.*

In this Project, configured the Zynq(ZC702) board for Processing System - Programmable logic Part and schedule the task according to real-time application and compare the task performance.

Minor Project: *Street View House Number Recognition Using CNN.*

In this project, our task was to recognize arbitrary multi–digit Numbers from street view image. To achieve this proposed target, we had used CNN with multi hidden layers that operated directly on image pixels .

Supervisor: *Dr. Vineet Sahula (Professor, ECE, MNIT, Jaipur)*

2013 -- 2017

B.E. in Electronics & Telecommunication Engineering: Jabalpur Engineering College, Jabalpur.
CGPA: **7.21**
Major Project: *Personal Weather Station*

In this project, our task was small place environmental quantity (like temperature, humidity, pressure etc.) show on online interface.

Minor Project: *Automatic Water Level Controller*

Any given pump set motor will Control the Operation of the pump set depending upon the water level in the source and destination storage tanks.

Education (continued)

2011 -- 2012   ■ **XII (Higher Secondary Examination): Rajendra Convent Higher Secondary School, Bhind.**
Percentage: **80.20**

2009 -- 2010   ■ **X (Secondary Examination): Rajendra Convent Higher Secondary School, Bhind.**
Percentage: **81.83**

Skills

Coding	■ C, Python.
Operating Systems	■ LINUX, WINDOWS.
HDL	■ VERILOG.
Embedded OS	■ FreeRTOS.
Multimedia Framework	■ GStreamer,FFmpeg.
Writing	■ LaTeX, Microsoft Office
Hardware Design Tools	■ XILINX-(VIVADO, FPGA EDITOR, SDK).
Hardware	■ NVIDIA JETSON TX2, XILINX ZYNQ(ZC702), ARTIX-7 (NEXYS 4 DDR, BASYS 3), STM32, RASPBERRY PI.

Miscellaneous Experience

Academic Achievements

2017   ■ I got Excellent Grade in "VLSI Design Verification Workshop".

2010   ■ I scored full marks in Mathematics (100 out of 100) in 10th standard.

Extra Curricular Activities

2016   ■ **Coordinator:** Coordinate in "Aureole'16" annual Techno-Cultural Fest held at JEC, Jabalpur.

2014   ■ **Coordinator:** Coordinate in "Blood Donation Camp" held at JEC, Jabalpur.

Workshops & Short-term Courses

Workshops & Short-term Courses

2018   ■ **EICT-Workshop**: Academic Training Programme on "ANN & Deep Learning" held at MNIT, Jaipur.

2017   ■ **EICT-Workshop**: Academic Training Programme on "VLSI Design Verification" held at MNIT, Jaipur.

■ **EICT-Workshop**: Academic Training Programme on "CAD Tools For Advanced Digital Design Using FPGA's" held at MNIT, Jaipur.