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Suresh Mopuri

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Date of Birth: 10-03-1990

EDUCATION:

- **PhD** in Microelectronics & VLSI,
  Indian Institute of Technology, Hyderabad, India. Graduated In: **Pursuing**
  CGPA: 8.5/10
- **B. Tech.** in Electronics and Communication Engineering,
  SV UNIVERSITY COLLEGE OF ENGG, India. Graduated In: May 2012
  CGPA: 8.21/10
- **Diploma** in Electronics and Communication Engineering,
  State board of technical education & training, AP, India. Graduated In: March 2008
  Grades: 78.57%
- **High School Degree,** Board of Secondary Education, AP, India. Graduated In: April 2005
  Grades: 86.67%

TECHNICAL SKILLS:

- **Operating Systems:** Ubuntu(Linux) and Windows
- **Languages:** VHDL, C, C++
- **Tools:** Mentor Graphics ModelSim, Xilinx ISE, Cadence RTL Compiler, Matlab

AREA OF INTEREST: Digital Electronic system design, VLSI signal processing, Low complexity system design, Digital signal processing, Radar signal processing, Architecture Design for signal processing algorithms.

WORK EXPERIENCE AND TRAINNING:

- Working at **SDSC SHAR / ISRO (Indian Space Research Organization) as a Scientist/Engineer since April 2014.**
  Laboratory: **RADAR-3 & MOTR, Range instrumentation systems.**
- Training at **Synopsys, Hyderabad** which is an Electronic Design Automation (EDA) tool design company: Our team was trained on the EDA tools like **Symphony C Compiler**, **Design Compiler** and **IC Compiler**.

PROJECTS:

- **Low complexity implementation of UBSS for emerging remote health care applications (ON GOING): This is my PhD thesis in IIT-H under my guide Dr Amit Acharyya.** Low complexity architecture for the Under-determined Blind Source Separation (UBSS) algorithm targeting remote healthcare applications. UBSS algorithm, departing from the typical BSS convention equal number of the sources and sensors present, which is of tremendous interest in the field of Biomedical signal processing especially for remote health care applications. Since such applications are constrained by the on chip area and power consumption limitation due to the battery backup, low complexity architecture needs to be formulated. In Multi Object Tracking Radar (MOTR) also UBSS applied to detect the targets.
- **Implementation of Digital Receiver& Radar Control unit for S-Band pulse compression radar (ON GOING):** This project is going in ISRO to track PSLV, GSLV, SLV, Sound rockets and balloons for weather observation**.**
- **Low Complexity Reconfigurable N-Point DHT (Discrete Hilbert Transform) Architecture Design Methodology :** This is my Embedded systems course project in IIT-H under my guide Dr Amit Acharyya
- **Mathematical model for 8 bit pipe line ADC:** This is my Analog IC Design course project in IIT-H under Dr Asudeb Dutta.
- **Fabrication of composite electrodes (CNT/PDMS) for ECG/EEG monitoring:** This is my Basics of Nano science and Technology course project in IIT-H under Dr Siva Vanjari and Dr CS Sharma.
- **Performance Estimation of LIDAR (Light detection and ranging):** This my B Tech project has done at NARL (National Atmospheric Research Laboratory)/ ISRO
- **Fiber optic based voice transmission system:** This is my Diploma project.