Mayank Kurveti
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Examination	University /Board	Institute	Year	CGPA/%
Graduation	IIT Hyderabad	IIT Hyderabad	2016*	7.08/10
Intermediate	MPBSE	Sai Sharda Public School	2012	81.2%
10th	CBSE	Maharishi Vidya Mandir	2010	9.20/10

*currently pursuing B.Tech in **Electrical Engineering**, IIT Hyderabad

Awards and Achievements:

Secured a place in Regional Mathematics Olympiad, got selected for Camp.
Merited in National Talent Search Contest, a CIGKL initiative.

Projects:

- *Fabrication of **Magnetic Quanta Cellular Automata based 3D IC TSV Design**.
- *µ-Total Analysis System** that scans the speed, flow, pattern of the pulse waves for diagnosing the casualties.
- **Avionics Full Duplex Ethernet Crossbar** logic for 2x2 ports.
- **FPGA Prototyping** for **Custom Ethernet IP Controller**.
- Low noise and low power **Neural Recording System** using NMOS as pseudo-resistance for Low Noise Amplifiers and optimum sizing of input pair of amplifier.
- An Ultrasonic SONAR using a **Nexys 3 FPGA** kit and basic ultrasonic range finder.
  *project still under process

Organizational Experience:

Intern, DRDO
Student Counselor, IIT Hyderabad
Creatives Coordinator, η vision
Member, Marketing Team, ELAN

Intern, Web Designer, **Kaddy**
TPC Coordinator, IIT Hyderabad
Web Designer, η vision

Undergone Courses:

Digital System Design
Analog Circuits
Digital Circuits
Analog IC Design

BioMedical IC Design*
Microprocessor and Computer Organization
Digital IC Design
VLSI Technology*

*courses under study in the current semester

Skill Set:

C, C++, **VHDL, Verilog**
Cadence, Synopsys

MATLAB, GEDA, G-Spice, **Adobe PS**
Xilinx ISE, Core Generator, PlanAhead