

V.CHARAN KUMAR**Phone:** +91-9000311130**Email:** ee14mtech01008@iith.ac.in**Web:** http://www.iith.ac.in/~amit_acharyya/Homepages/Charan_Kumar.html

CARRER OBJECTIVE:

To pursue a responsible and challenging position in a results-oriented company, where acquired Skills and education will be utilized towards continued growth and advancement.

ACADEMIC QUALIFICATION:

Course	Name & college	Board/ University	Year of passing	CGPA/ percentage
M.TECH (Microelectronics & VLSI)	IIT Hyderabad	IIT Hyderabad	2016 (pursuing)	8.6 (up to 2 nd sem)
B.TECH(E.C.E)	RRS college of Engineering & Technology	JNTU Hyderabad	2012	75.81%
INTERMEDIATE (M.P.C)	Narayana Junior College	Board of Intermediate education	2008	88.2%.
S.S.C	Sri Sathya Sai Vidyalayam	Board of secondary Education	2006	74.5%

ACHIVMENTS:

- Secured 98.33, 96.30 percentile in GATE 2013&2012 respectively.
- Won Mandal level first prize in school level Kho-Kho game.
- BALAVIKAS Group I & II certified from SRI SATHYA SAI SEVA SAMITHI.

COURSES:

- Digital IC design & verification, Analog IC design, VLSI technology, Semiconductor Device Modeling, DSP, Embedded Systems, Advanced Topics in Digital Circuit Design, Embedded Memory design, VLSI design lab, Microelectronics lab, DSP lab, Semiconductor device modeling lab

TECHNICAL SKILLS:

- Software Language : C, Embedded C, Assembly language for 8085 .
- HDL : Verilog
- Tools : Xilinx ISE Design Suit, ModelSim, Cadence Virtuoso, Matlab, Silvaco TCAD, CCStudio
- Development kits: ARM LXP1768, Xilinx FPGA Spartan-6, TMS320C6713 DSK.

EXTRA CURRICULAR ACTIVITES:

- I have been Participated in social services through SRI SATHYA SAI SEVA SAMITH
- Worked as a hostel representative for academic year 2014-2015 at IIT HYDERABAD
- Worked as TA for Embedded Programming course at IIT HYDERABAD
- Held leadership role at school level & college level

STRENGTHS:

- Dedication and Hardworking.
- Passion to learn with self-confidence.

PROJECT OVERVIEW:

M.Tech:

- Currently working on “**Efficient Power Management System Design For Low Power Microprocessors and ASIC’s**” under the guidance of Dr.Amit Acharyya.
Now a day’s most of the systems are self-powered and most of them are running on battery power (wireless sensor networks). Since these devices are deployed in places where use of wires for communication or power supply is very costly or even unfeasible at the same time Battery replacement is undesirable due to its cost. So to prolong battery life time we are going to design a systems such that, it controls the power supply adaptively based on frequency of operation.
- **Low Complexity Personalized and Remote Cardiovascular Disease Monitoring System Design (August 2014 to January 2015 Under the guidance of Dr. Amit Acharyya):**
The main aim of project is to find out the ECG signal is normal/abnormal and gives the information about ECG signal to the doctor mobile phone by using ARM LXP1768 along with Bluetooth module. The algorithms used for this are Boundary Detection, Feature Extraction ,Rule Engine, Data Compression
- **Design of 256x64 6T SRAM Array Using Cadence Virtuoso(June 2014 Under the guidance of Dr. Bharadwaj Amrutur).**
- **Simulation of short channel effects in SOI MOSFETS Using TCAD(August -November 2014 Under the guidance of Dr. Shiv Govind Singh).**

B.Tech :

- **Out Door Security System(January -April2012):**
This project is based on AT89S52 microcontroller. The microcontroller, Ultra Sonic Sensor, Read switches, are plays an important role in the System. The main objective of the system is to provide the range and direction Any objective which is coming in to restricted areas.
- **Home Security System (May -July 2011):**
This project is based on 8051 family microcontroller. The microcontroller, IR Transmitter, IR Receiver plays an important role in the system. The main objective of the system is to give an alarm when any person entering in to the home.

PERSONAL DETAILS:

DOB : 06-07-1991
Gender : Male
Marital Status : Single
Nationality : Indian
Address : H.No-314,C-Block,Sai Krupa Aparts ,
Madenaguda , Miyapur ,Hyderabad, Telangana 500049
Languages Known : English and Telugu

Date:

Place: Hyderabad

(V.CHARAN KUMAR)